- 3. The method according to claim 2, wherein the at least one processor is coupled to the image sensor, wherein the at least one processor performs the method using the image sensor.
- **4**. The method according to claim **1**, wherein the region of interest is a region of the image sensor that images a drip chamber.
- 5. The method according to claim 1, wherein the region of interest corresponds to a drip chamber.
- **6**. The method according to claim **1**, wherein the activating act activates a subset of lights including the light of the backlight.
- 7. The method according to claim 1, wherein the light of the backlight forms a uniform backlight.
  - 8. The method according to claim 1, further comprising: receiving a vertical sync signal from the image sensor; and

receiving a horizontal sync signal from the image sensor.

- **9**. The method according to claim **8**, wherein the at least one processor receives the vertical and horizontal sync signals from the image sensor.
- 10. The method according to claim 9, wherein the at least one processor activates the light of the backlight in accordance with at least one of the vertical and horizontal sync signals.
- 11. The method according to claim 9, wherein the light is a light-emitting diode.
- 12. The method according to claim 1, wherein the image sensor includes the region of interest and the pixel.
  - 13. A flow meter, comprising:
  - a coupler adapted to couple to a drip chamber;
  - a support member operatively coupled to the coupler;
  - an image sensor having a field of view and operatively coupled to the support member, wherein the image sensor is positioned to view the drip chamber within the field of view;
  - a backlight having at least one light, wherein the backlight is coupled to the support member, wherein the backlight is adapted to illuminate the image sensor to expose the image sensor such that the field of view of the image sensor at least partially images at least a portion of the drip chamber; and

- at least one processor operatively coupled to the image sensor to receive image data therefrom, wherein the at least one processor is configured to:
  - select a region of interest of the image sensor,
  - determine whether a pixel of the image sensor is within the region of interest,
  - activate the at least one light of the backlight when the pixel of the image sensor is within the region of interest, and

expose the pixel of the image sensor.

- 14. The flow meter according to claim 13, further comprising a non-transitory processor-readable memory readable by the at least one processor, wherein the non-transitory processor-readable memory includes an operative set of processor executable instructions stored thereon configured to cause the at least one processor, when executed, to: select the region of interest of the image sensor; determine whether the pixel of the image sensor is within the region of interest; activate the at least one light of the backlight when the pixel of the image sensor is within the region of interest; and expose the pixel of the image sensor.
- 15. The flow meter according to claim 13, wherein the at least one processor selects the region of interest and determines if the pixel of the image sensor is within the region of interest in accordance with the image data.
- **16**. The flow meter according to claim **13**, wherein the region of interest is a region of the image sensor that images the drip chamber.
- 17. The flow meter according to claim 13, wherein the region of interest corresponds to the drip chamber.
- 18. The flow meter according to claim 13, wherein the at least one processor activates a subset of lights including the at least one light of the backlight.
- 19. The flow meter according to claim 13, wherein the at least one light of the backlight forms a uniform backlight.
- 20. The flow meter according to claim 13, wherein the at least one processor is further configured to: receive a vertical sync signal from the image sensor; and receive a horizontal sync signal from the image sensor.
- 21. The flow meter according to claim 20, wherein the at least one processor is further configured to activate the at least one light of the backlight in accordance with at least one of the vertical and horizontal sync signals.

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